IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

TAJIRI, M.

Filed:

For:

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November 5, 2003 NONVOLATILE VARIABLE

RESISTOR, MEMORY DEVICE, AND

SCALING METHOD OF

NONVOLATILE VARIABLE

RESISTOR

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Examiner:

Lam, D.

MAIL STOP IF Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

As suggested by 37 C.F.R. 1.97, the undersigned attorney brings to the attention of the United States Patent and Trademark Office the references listed on the attached form PTO-1449, a copy of which is enclosed. U.S. Patents and Publications are not necessary and are not attached. U.S. Publication 2004/0036109 corresponds to the JP 2004-87069 reference. Although no certification is being made under 37 C.F.R. §1.97(e) nor fee rendered, Applicants fulfill their duty of candor by this filing.

> Respectfully submitted, NIXON & VANDERHYE P.C.

> > Halles

June 5, 2008

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